

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

S. Patent Application of:

INVENTORS: Ruha et al.

SERIAL NO.: 10/005,766

ART UNIT: 2182

FILED: November 2, 2001

EXAMINER: Unknown

TITLE: "Multi-mode I/O Circuitry Supporting Low Interference Signaling Schemes for High Speed

Digital Interfaces"

ATTORNEY DOCKET NO.: 872.0100.U1(US)

Hon. Commissioner of Patents and Trademarks Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

Each document contained in the accompanying information disclosure statement was cited in a communication from a foreign office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. A copy of the Search Report is enclosed.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted

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Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.

Include a copy of this citation form with your next correspondence to the Applicant(s).